



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:	Confirmation No.: 6813
Kinter <i>et al.</i>	Art Unit: 2183
Appl. No.: 09/836,541	Examiner: Pan, Daniel H.
Filed: April 18, 2001	Atty. Docket: 1778.0200000
For: <b>Mapping System and Method for Instruction Set Processing</b>	

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Technology Center 2100

Commissioner for Patents  
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Alexandria, VA 22313-1450

Sir:

Listed on accompanying Form PTO-1449 are documents that may be considered material to the examination of this application, in compliance with the duty of disclosure requirements of 37 C.F.R. §§ 1.56, 1.97 and 1.98. This Information Disclosure Statement is supplemental to an electronic Information Disclosure Statement filed the same day. Any fee necessary for consideration of this Information Disclosure Statement has been authorized or otherwise submitted with the electronic Information Disclosure Statement.

Applicants have listed publication dates on the attached PTO-1449 based on information presently available to the undersigned. However, the listed publication dates should not be construed as an admission that the information was actually published on the date indicated.

Applicants reserve the right to establish the patentability of the claimed invention over any of the information provided herewith, and/or to prove that this information may not be prior art, and/or to prove that this information may not be enabling for the teachings purportedly offered.

This statement should not be construed as a representation that a search has been made, or that information more material to the examination of the present patent application does not exist. The Examiner is specifically requested not to rely solely on the material submitted herewith.

This Information Disclosure Statement is being filed under 37 C.F.R. § 1.97(c) more than three months after the U.S. filing date and after the mailing date of the first Office Action on the merits, but before the mailing date of a Final Rejection, or Notice of Allowance, or an action that otherwise closes prosecution in the application. Payment of the fee under 37 C.F.R. § 1.17(p) has been authorized or otherwise submitted with an electronic Information Disclosure Statement filed today.

Copies of the cited documents are submitted herewith.

It is respectfully requested that the Examiner initial and return a copy of the enclosed PTO-1449, and indicate in the official file wrapper of this patent application that the documents have been considered.

Kinter *et al.*  
Appl. No. 09/836,541

The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.



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Date: 9/28/04

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FORM PTO-1449

INFORMATION DISCLOSURE STATEMENTATTY. DOCKET NO.  
1778.0200000APPLICATION NO.  
09/836,541FIRST NAMED INVENTOR  
Ryan C. KinterFILING DATE  
April 18, 2001ART UNIT  
2183**U.S. PATENT DOCUMENTS**

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA1					
	AB1					
	AC1					
	AD1					
	AE1					
	AF1					
	AG1					
	AH1					
	AI1					
	AJ1					
	AK1					

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**FOREIGN PATENT DOCUMENTS**

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
AL1	0 109 567 A2	05/1984	EP			N/A
AM1	0 170 398 A2	02/1986	EP			N/A
AN1	WO 95/30187 A1	11/1995	WO			N/A
AO1						Yes No
AP1						Yes No

**OTHER (Including Author, Title, Date, Pertinent Pages, etc.)**

AR	1	Hirata, H., et al., "An Elementary Processor Architecture with Simultaneous Instruction Issuing from Multiple Threads," ACM SIGARCH Computer Architecture News, Volume 20, Number 2, pgs. 136-145, Association for Computing Machinery (May 1992).
AS	1	
AT	1	

EXAMINER

DATE CONSIDERED

**EXAMINER:** Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.